

FIG. 1

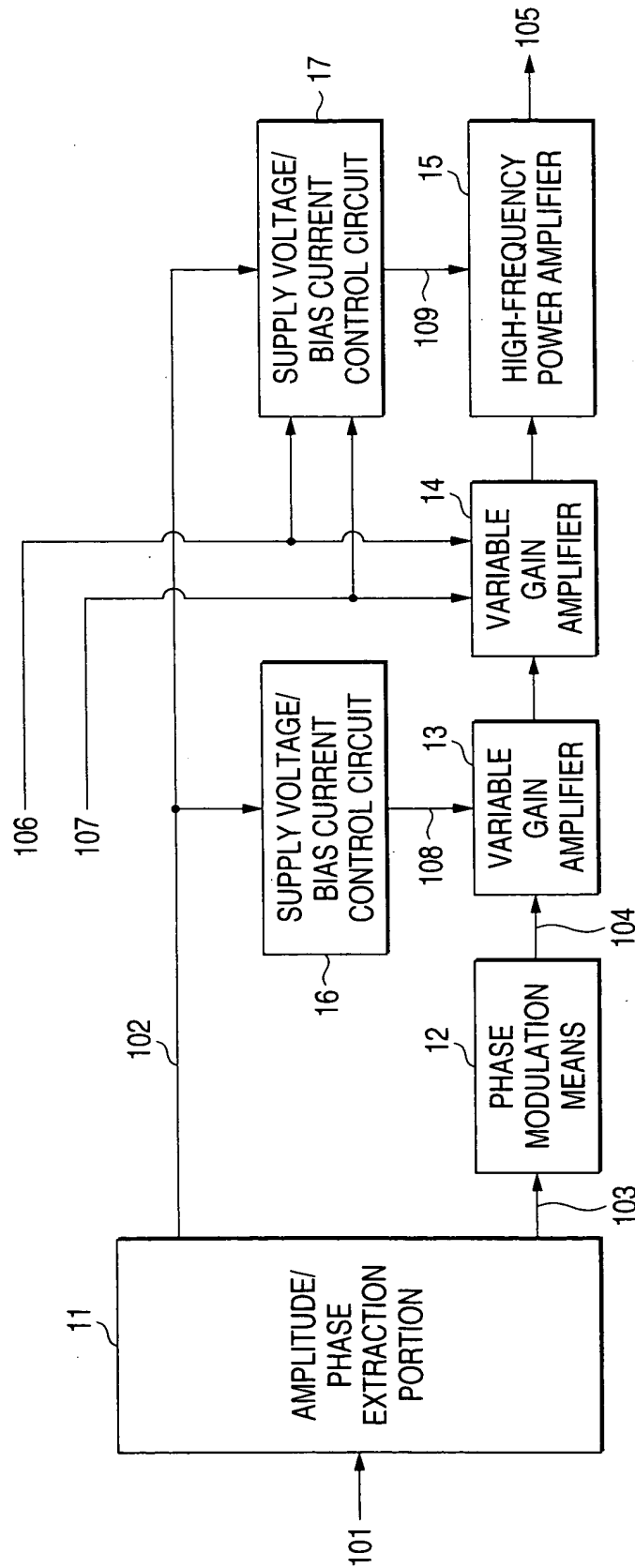
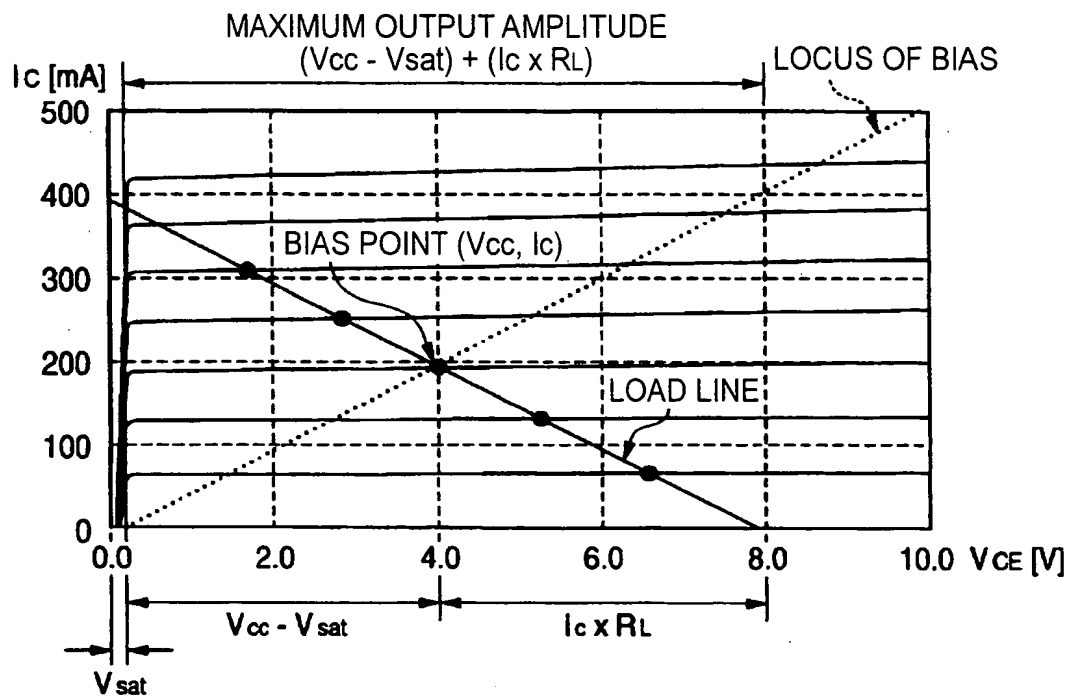


FIG. 2



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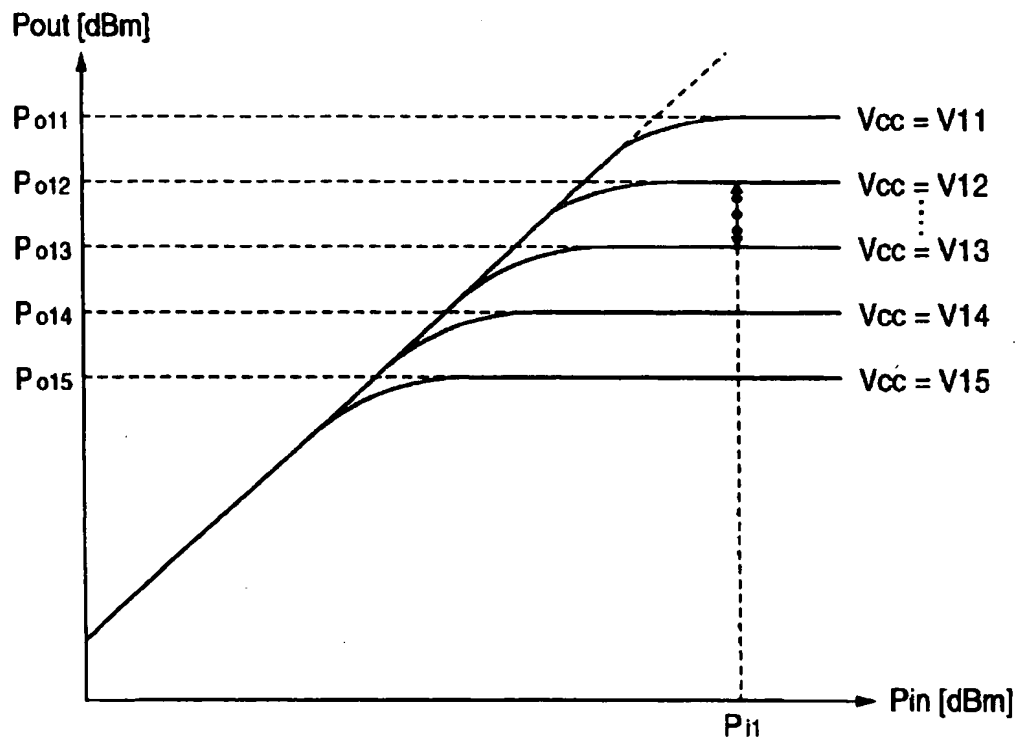
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FIG. 3



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FIG. 4

TIME	LINER OPERATING MODE			SATURATION OPERATING MODE		
	GAIN OF VARIABLE GAIN AMPLIFIER 14	VOLTAGE OF SUPPLY VOLTAGE/BIAS CURRENT CONTROL CIRCUIT 17	CURRENT OF SUPPLY VOLTAGE/BIAS CURRENT CONTROL CIRCUIT 17	GAIN OF VARIABLE GAIN AMPLIFIER 14	VOLTAGE OF SUPPLY VOLTAGE/BIAS CURRENT CONTROL CIRCUIT 17	CURRENT OF SUPPLY VOLTAGE/BIAS CURRENT CONTROL CIRCUIT 17
t(1, 1)	g ₁	V ₁	I ₁	g ₁ + α	V(1, 1)	I(1, 1)
t(1, 2)	g ₁	V ₁	I ₁	g ₁ + α	V(1, 2)	I(1, 2)
t(1, 3)	g ₁	V ₁	I ₁	g ₁ + α	V(1, 3)	I(1, 3)
t(1, 4)	g ₁	V ₁	I ₁	g ₁ + α	V(1, 4)	I(1, 4)
t(1, 5)	g ₁	V ₁	I ₁	g ₁ + α	V(1, 5)	I(1, 5)
t(1, 6)	g ₁	V ₁	I ₁	g ₁ + α	V(1, 6)	I(1, 6)
⋮	⋮	⋮	⋮	⋮	⋮	⋮
t(n, 1)	g _n	V _n	I _n	g _n + α	V(n, 1)	I(n, 1)
t(n, 2)	g _n	V _n	I _n	g _n + α	V(n, 2)	I(n, 2)
t(n, 3)	g _n	V _n	I _n	g _n + α	V(n, 3)	I(n, 3)
t(n, 4)	g _n	V _n	I _n	g _n + α	V(n, 4)	I(n, 4)
t(n, 5)	g _n	V _n	I _n	g _n + α	V(n, 5)	I(n, 5)
t(n, 6)	g _n	V _n	I _n	g _n + α	V(n, 6)	I(n, 6)
⋮	⋮	⋮	⋮	⋮	⋮	⋮

FIG. 5

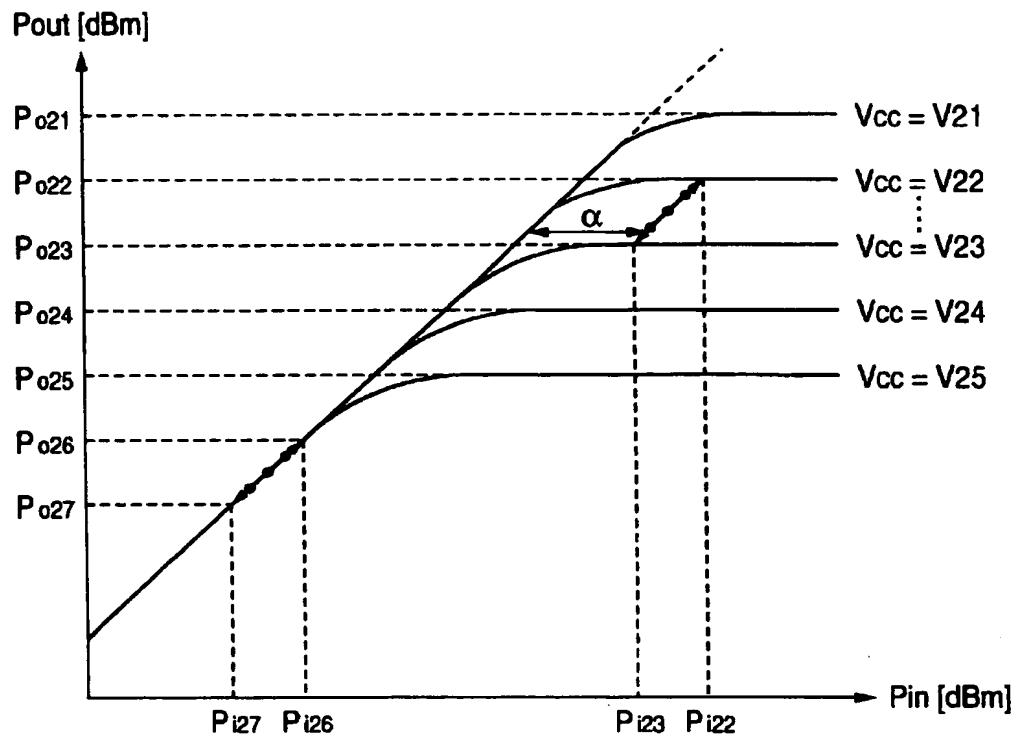


FIG. 6

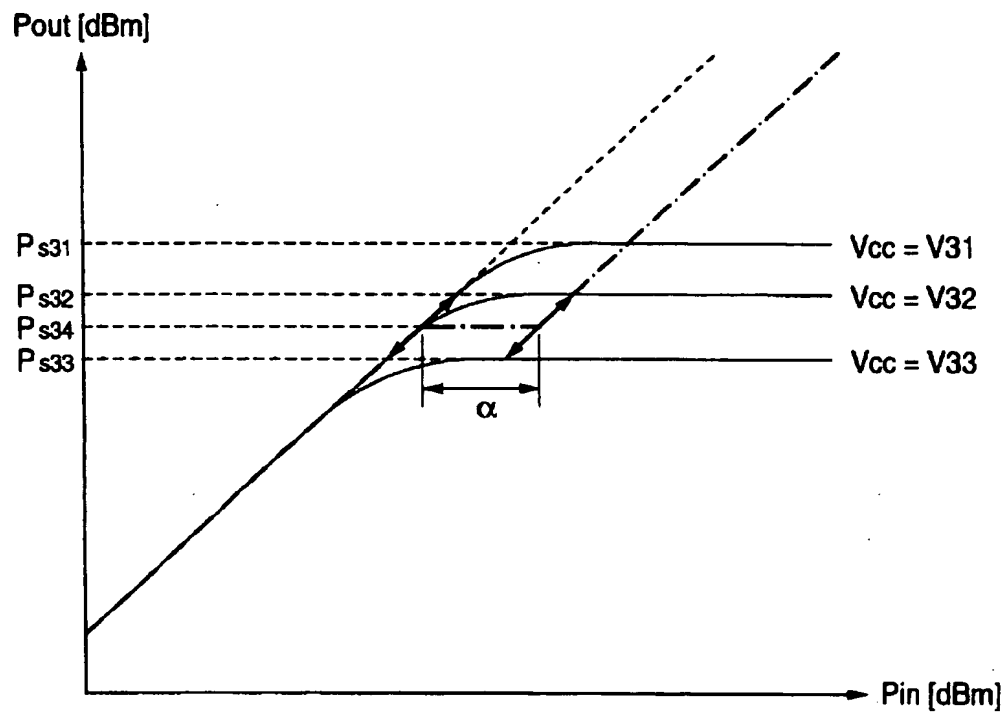


FIG. 7

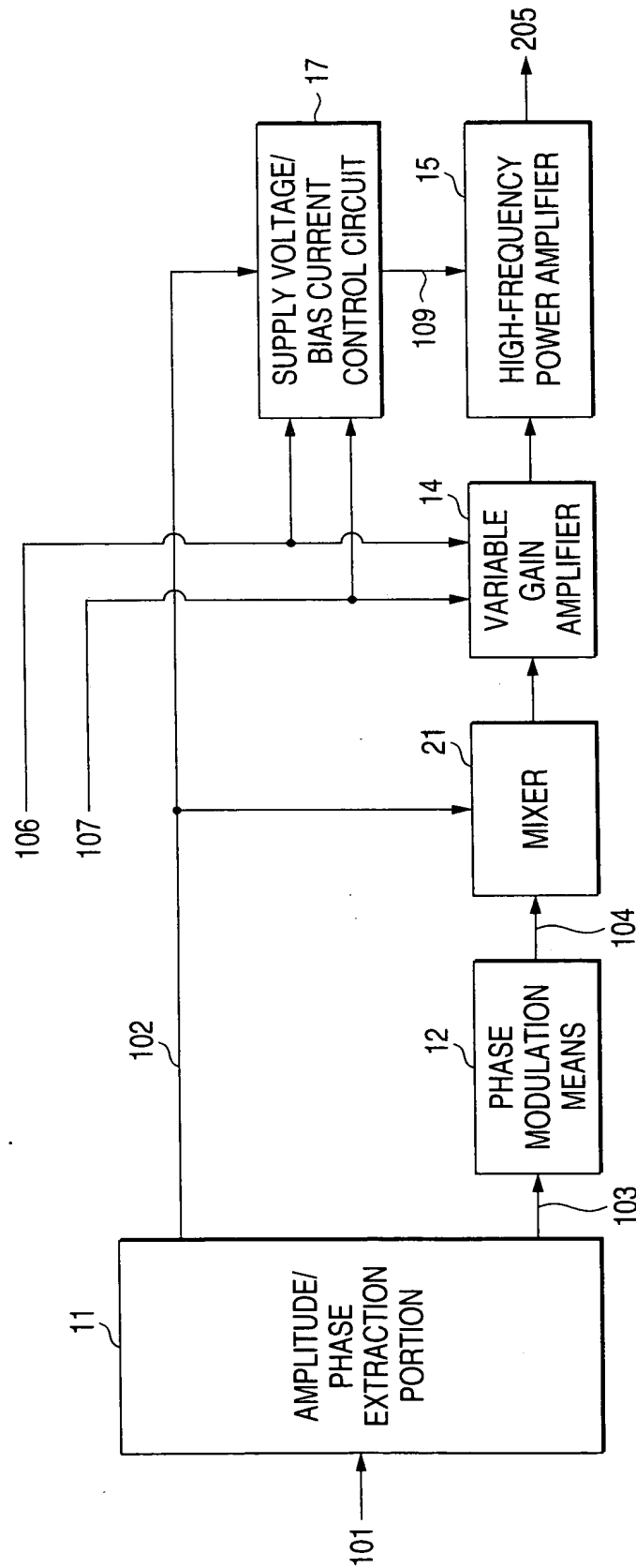
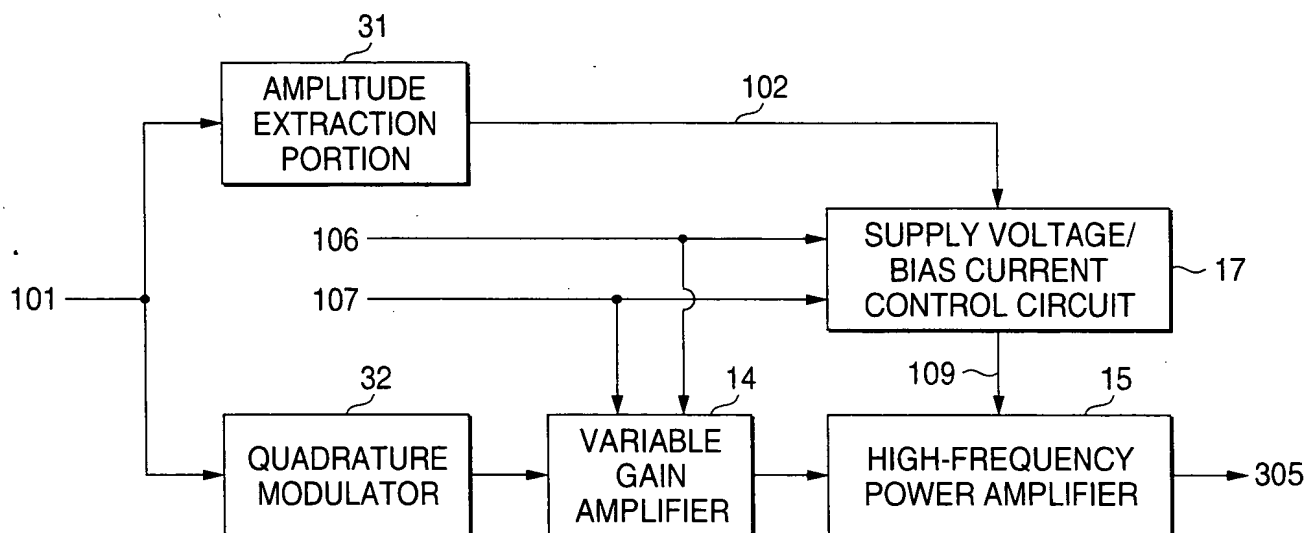


FIG. 8



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FIG. 9

